



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Makoto YOSHIDA et al.
 Serial No.: 10/653,889
 Filed: September 4, 2003
 For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
 MANUFACTURING METHOD OF MINIATURIZATION OF
 MEMORY CELLS WITH REDUCED BIT LINES (As Amended)**
 Group: 2814
 Examiner: Hoai V. Pham

PETITION FOR EXTENSION OF TIME**Mail Stop: RCE**

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

September 23, 2005

Sir:

In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing a Request for Continued Examination within the third-month time period subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed **March 23, 2005**. A credit card payment form in the amount of \$1,020.00 to cover the required fee for the requested extension of time is attached hereto.

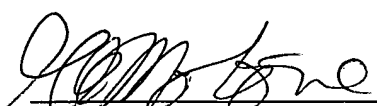
It is respectfully requested that any shortage in the fee be charged to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Docket No. 501.37959CC2), and any excess fees credited to that account.

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Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP

By 
 Gregory E. Montone
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